MULTIPLEXER

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Inventor: BABA KOICHIRO; IRIBE SHINICHI

Applicant: HITACHI LTD

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- international: H04J3/17; H04L5/22; H04J3/17; H04L5/00; (IPC1-7): H04J3/17; H04L5/22; H04L12/48

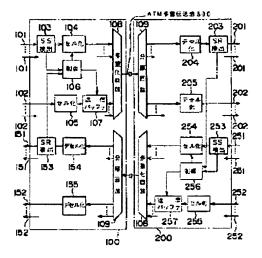
- european:

Application number: JP19920161474 19920619 **Priority number(s):** JP19920161474 19920619

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Abstract of JP6006320

PURPOSE:To use the band efficiently by sending data to be sent when the data to be sent are in existence in the non-use state where no valid information is sent to a transmission line. CONSTITUTION:An SS signal being a voice control signal from a telephone set via an exchange is set to a ground level and a start signal is sent. An SS signal detection circuit 103 of a multiplexer 100 detects that the SS signal is set to a ground level and informs it to a control means 106. When the SS signal is set to a ground level, the control means 106 controls a transmission buffer 107 not to send data and commands cell processing of the voice signal to a voice cell processing circuit 104. The circuit 104 processes a time slot of the SS signal and the voice signal into a cell. A multiplexer circuit 108 multiplexes and sends the result. Furthermore, when the telephone set is not busy, the means 106 controls a buffer 107 to send a data cell.



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